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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	10/814,568
		Filing Date	March 31, 2004
		First Named Inventor	Tsai, Vicki
		Art Unit	2825
		Examiner Name	Whitmore, Stacy
Sheet 1	of 1	Attorney Docket No:80107.117US1	

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
<i>[Signature]</i>		US-20050149890A1	07/07/2005	Tsai, V. W., et al.	12/29/2003
<i>[Signature]</i>		US-20050223110A1	10/06/2005	Honary, H. et al.	03/30/2004

FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
<i>[Signature]</i>	WO-0038087A1	06/29/2000	Saul, J. M., et al.	
<i>[Signature]</i>	WO-05098649A2	10/20/2005	Tsai, V., et al.	
<i>[Signature]</i>	WO-05098649A3	10/20/2005	Tsai, V., et al.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>[Signature]</i>		International Search Report and Written Opinion of the International Searching Authority; Dated 10/07/2005; PCT/US2005/010135, (14 pgs).	
<i>[Signature]</i>		AHMADINIA, A., et al., "Temporal Task Clustering for Online Placement on Reconfigurable Hardware", <u>Field-Programmable Technology (FPT), 2003. Proceedings IEEE International Conference</u> , XP010688364,(Dec. 15, 2003),359-362	
<i>[Signature]</i>		BLUME, H., et al., "Model-based Exploration of the Design Space for Heterogeneous System on Chip", <u>Proceedings of the IEEE International Conference on Application-Specific Systems, Architectures and Processors</u> , 2002., XP010601457,(Jul. 17, 2002),29-40	
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<i>[Signature]</i>		SCIUTO, D., et al., "Metrics for Design Space Exploration of Heterogeneous Multiprocessor embedded Systems", <u>Proceedings of the 10th International Workshop on Hardware/Software Codesign</u> , XP002317354,(May 6, 2002),55-60	

EXAMINER	<i>[Signature]</i>	DATE CONSIDERED	3/6/06
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Substitute Disclosure Statement Form (PTO-1449)
 * EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached